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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Reimer et al

Art Unit: 2111

Serial No.: 10/008,696

Examiner: Khanh Dang

Filed: November 8, 2001

Docket: TI-30105

For: MULTICORE DSP DEVICE HAVING COUPLED SUBSYSTEM MEMORY BUSES FOR  
GLOBAL DMA ACCESS


FFICIAL

AMENDMENT UNDER 37 C.F.R. 1.111

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

CERTIFICATION OF FAX TRANSMITTAL  
UNDER 37 C.F.R. §1.6(b)

I hereby certify that the above  
correspondence is being facsimile  
transmitted to the Patent and Trademark  
Office on August 4, 2004.

  
Robin E. Barnum

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated May 4, 2004. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows: